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L1	1650	716/1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/10 13:20
L2	1	(716/1).ccls. and ((standard adj cells) same (variant)) and (manufactur\$3 adj environment) and (yield same ((integrated adj circuit) or IC))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/10 13:21
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S2	2070	716/4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/10 12:21
S3	1633	716/5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/10 12:21
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S6	0	(716/5).ccls. and ((standard adj cells) same (variant)) and (manufactur\$3 adj environment) and (yield same ((integrated adj circuit) or IC))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/10 12:26
S7	1	("716"/\$).ccls. and ((standard adj cells) same (variant)) and (manufactur\$3 adj environment) and (yield same ((integrated adj circuit) or IC))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/10 12:28

S8	2	((standard adj cells) same (variant)) and (manufactur\$3 adj environment) and (yield same ((integrated adj circuit) or IC))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/10 12:29
S9	2	((standard adj cells) same (variant)) and (manufactur\$3 adj problem) and (yield same ((integrated adj circuit) or IC))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/10 12:30
S10	1	((standard adj components) same (variant)) and (manufactur\$3 adj problem) and (yield same ((integrated adj circuit) or IC))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/10 12:31
S11	1	((standard adj components) same (variant adj designs)) and (manufactur\$3 adj problems) and (yield same ((integrated adj circuit) or IC))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/10 12:36
S12	1	((standard adj components) same (variant adj designs)) and (manufactur\$3 adj problems) and (yield same ((integrated adj circuit) or IC)) and rat\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/10 12:37

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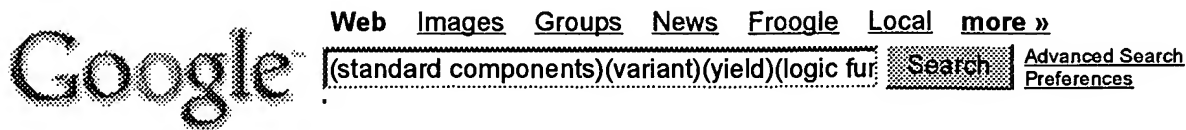
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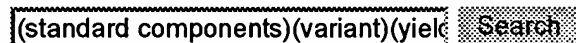
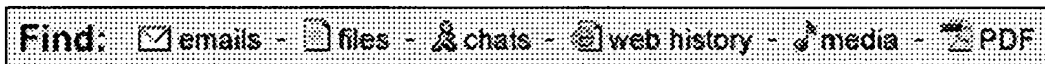
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